



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Seong-Ho Jeung

DOCKET: 8021-196 (SS-19274-US)

SERIAL NO.: 10/756,715

GROUP ART UNIT: 2824

FILED: January 13, 2004

EXAMINER: Hur, Jung H.

FOR: SEMICONDUCTOR DEVICE AND TESTING METHOD

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

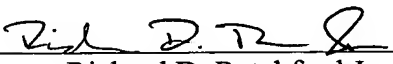
Sir:

In response to the Office Action dated July 14, 2005, Applicants provisionally elect the claims of Group I (i.e., claims 1-15) with traverse.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Assistant Commissioner for Patents, Washington, D.C. 20231 on August 12, 2005.

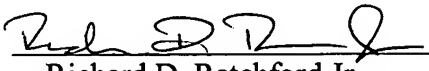
Dated: 8/12/05


Richard D. Ratchford Jr.

While the device of Group I and the method of Group II may be distinct as set forth by the Examiner, Applicants believe that simultaneous examination will not present an undue burden. For example, the claims of Group I are drawn to a semiconductor device, classified in class 365, subclass 189.09 and the claims of Group II are drawn to a method of testing a semiconductor device, classified in class 365, subclass 201. The two groups contain subject matters that are commonly classified in class 365. Under such circumstances, the Examiner is encouraged to maintain the claims of Groups I and II in the same application. See, MPEP § 803.

Early and favorable consideration of the application is earnestly solicited.

Respectfully submitted,

By: 
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